



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
BASOTTI ET AL. )  
Serial No. 10/748,697 )  
Confirmation No. 2009 )  
Filing Date: December 30, 2003 )  
For: VOLTAGE SUPPLY DISTRIBUTION )  
ARCHITECTURE FOR A PLURALITY )  
OF MEMORY MODULES )

REQUEST FOR CORRECTION TO THE FILING RECEIPT

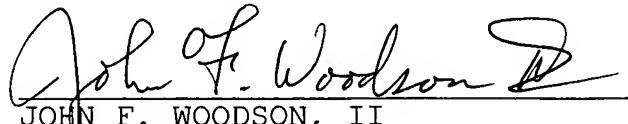
MS Missing Parts  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. A discrepancy has been discovered. The attorney docket number is incorrectly listed as "02AG39153434". The correct attorney docket number is **02AG39153424**.

In view of the error noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. **01-0484**.

Respectfully submitted,

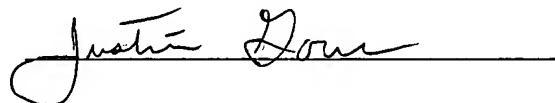
  
JOHN F. WOODSON, II

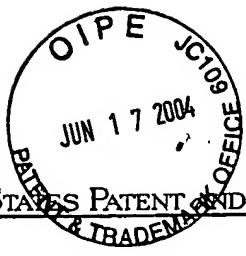
Reg. No. 45,236  
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255 S. Orange Avenue, Suite 1401  
Post Office Box 3791  
Orlando, Florida 32802  
407/841-2330  
Attorney for Applicants

In the Patent Application of:  
**PASOTTI ET AL.**  
Serial No. **10/748,697**  
Filing Date: **December 30, 2003**

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MS Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 15<sup>th</sup> day of June, 2004.

  
\_\_\_\_\_  
Justin Dorn



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/748,697	12/30/2003	2818	0.00	02AG39153434	2	25	4

02AG39153434

CONFIRMATION NO. 2009

27975

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FILING RECEIPT



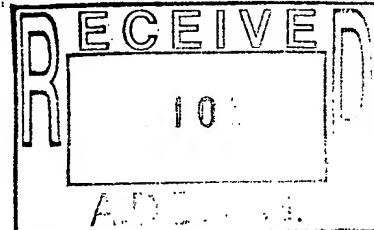
\*OC000000012552424\*

Date Mailed: 05/06/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

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**Assignment For Published Patent Application**

STMicroelectronics S.r.l., Agrate Brianza (MI), ITALY;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

EUROPEAN PATENT OFFICE (EPO) 02425809.7 12/30/2002

**If Required, Foreign Filing License Granted: 05/05/2004**

**Projected Publication Date: To Be Determined - pending completion of Missing Parts**

**Non-Publication Request: No**

**Early Publication Request: No**

**Title**

Voltage supply distribution architecture for a plurality of memory modules

**Preliminary Class**

365

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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